

k^+ -buffer: Fragment Synchronized k-buffer

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Outline

- 1 Introduction
- 2 Framework Overview
- 3 Experimental Study
- 4 Conclusions & Future Work

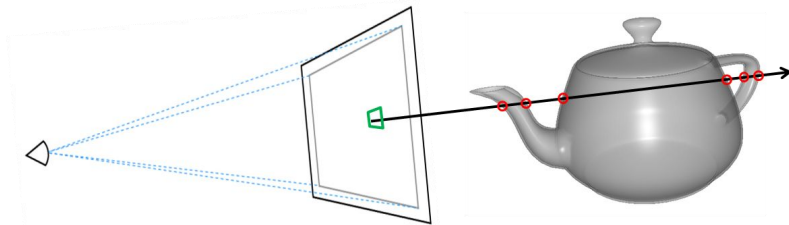
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Visibility Determination

A number of image-based applications require operations on more than one (maybe occluded) fragment per pixel:

- transparency effects¹ - volume & CSG rendering²
- collision detection³
- visualization of coplanar⁴ & self-trimming surfaces⁵
- shadows⁶ - hair rendering⁷



¹[Maule et al., CG'11], ²[Bavoil et al., I3D'07], ³[Jang et al., VC'08], ⁴[Vasilakis et al., TVCG'13], ⁵[Rossignac et al., CAD'13], ⁶[Yang et al., CGF'10], ⁷[Yu et al., I3D'12]

Prior Art - Contribution

A-buffer¹ & variants (FreePipe², LL³, LL-Paged⁴, SB⁵)

- capture [all] fragments per pixel $\mapsto$ sort them by depth

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- ① overcomes all limitations of existing k -buffer alternatives
- ② memory-friendly variation (extra pass needed)
- ③ supports Z-buffer and A-buffer functionalities

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k^+ -buffer Pipeline

Store & Sort solution:

- 1 captures fragments in an unsorted sequence

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1. Store Rendering Pass:

- spin-lock strategy ([binary semaphores]¹ or [pixel sync]²)
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- two bounded array-based structures:
 - [early-fragment culling] - $O(1)$

example

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 - [early-fragment culling] - $O(1)$
 - if $k < 16$: [max-array]-(K^+ B-Array), else [max-heap]-(K^+ B-Heap)
 - [insert()]: $O(1)$ vs $O(\log_2 k)$ - [find_max()]: $O(k)$ vs $O(\log_2 k)$

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2. Sort Full-screen Pass:

- if $k < 16$: [insertion-sort], else [shell-sort]

Extending k^+ -buffer

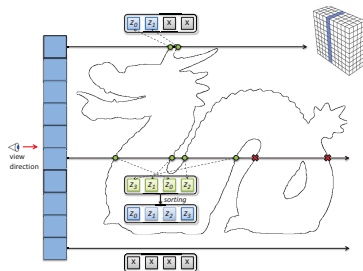
Precise memory allocation

- k is the same for [all] pixels

Extending k^+ -buffer

Precise memory allocation

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- [Idea]: S-buffer(SB)¹ - (K^+B -SB)
 - count pass $\mapsto$ [hybrid scheme]
 - if [counter] reaches k stop
 - extra pass & shared memory

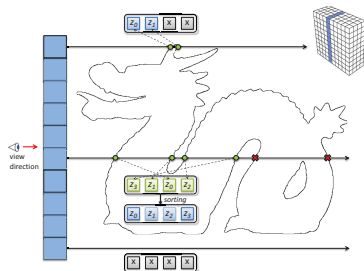


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Unified framework

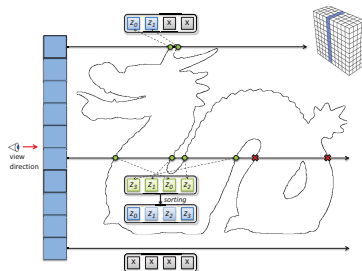
- adjust the value of k :
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Unified framework

- adjust the value of k :
 - $k = 1$: [Z-buffer] behavior
 - $k = \max_p \{f(p)\}$: [A-buffer] behavior:
 - $K^+B \mapsto \text{FreePipe}^2$
 - $K^+B\text{-SB} \mapsto \text{SB}^1$

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Testing Environment

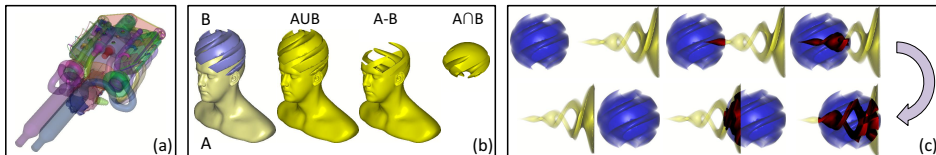
- [artificially generated scenes]: $n = r \cdot k, r \geq 1$
- [screen resolution]: 854×480 (16:9) - [pixel density]: p_d
- OpenGL 4.3 API - NVIDIA GTX 480 (1.5 GB memory)

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Applications

- (a) OIT¹, (b) CSG², (c) Collision Detection³

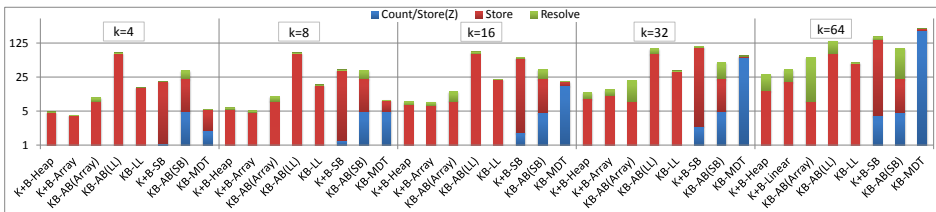


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Performance Analysis - k -buffer

Impact of k (milliseconds)

- [Scene]: $n = 128$, $k = \{4, \dots, 64\}$
- K^+B -Array [$k \downarrow$] vs K^+B -Heap [$k \uparrow$]
- KB-MDT¹ (two-pass method): future 64-bit atomic operations?
- KB-AB_{Array}: storing [$k \uparrow$] - sorting [$k \downarrow$]

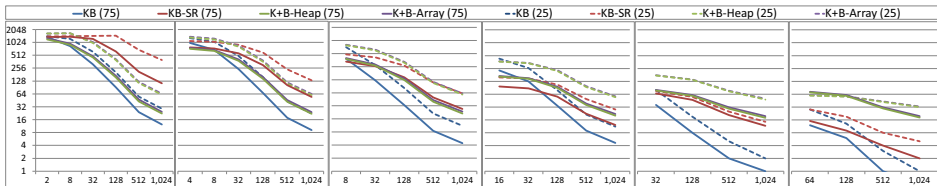


¹[Maule et al., I3D'13]

Performance Analysis - k -buffer

Impact of Sorting (fps)

- [Scene]: $n = \{k, \dots, 1024\}$, $p_d = \{25\%, 75\%\}$, [depth sorted]
- $K^+B\text{-Array}$ ($O(1)$) $>$ $K^+B\text{-Heap}$ ($O(\log_2 k)$) - ($[p_d \uparrow]$: linear behavior)
- $[k \uparrow] \mapsto$ [multi-pass rendering]: $K^+B > KB\text{-SR}^2 > KB^1$
- $K^+B > KB^1 > KB\text{-PS}^3$

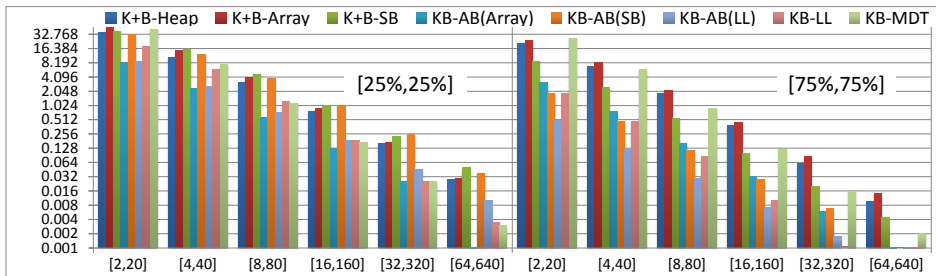


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Performance Analysis - k -buffer

Impact of Memory (fps/MB)

- [Scene]: $n = \{k, \dots, 10 \cdot k\}$, $[p_d, f_p]$
- K^+B -SB $[p_d \downarrow, f_p \downarrow]$ vs K^+B $[p_d \uparrow, f_p \uparrow]$
- $k = 64$: A-buffer-based solutions¹ fail (memory overflow)

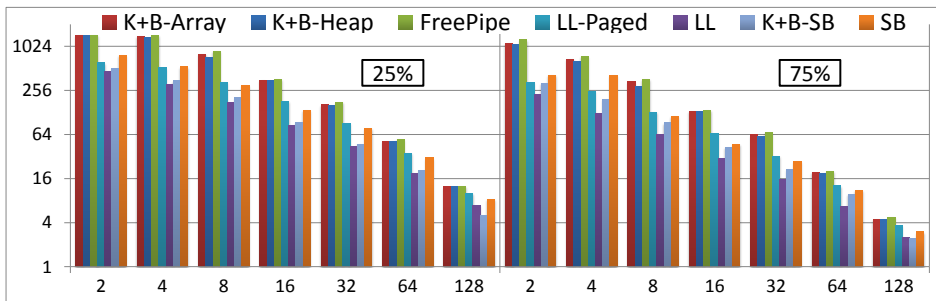


¹[Yu et al., I3D'12]

Performance Analysis - A-buffer

Impact of k (fps)

- [Scene]: $k = n, p_d = \{25\%, 75\%\}$
- FreePipe¹ > K⁺B-Array > K⁺B-Heap > rest methods^{2,3,4} (culling)
- SB⁴ > K⁺B-SB > LL² (if condition at counting pass)



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Memory Allocation Analysis

[table](#)

Comparison between [bounded-buffers]

- $\text{KB-AB}_{\text{Array}}$ needs huge resources
- K^+B vs $\{\text{KB}^1, \text{KB-PS}^3\}$: more storage (8-byte/pixel)
- [pixel sync] avoids semaphore allocation (4-byte)
- [data packing] employed: $\forall k > 1 : 4k > 2k + 2$
- K^+B vs KB-SR^2 : $\forall k > 2 : 3k > 2k + 2$

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Comparison between [unbounded-buffers]

- $K^+B\text{-}SB$ requires:

① [equal]: $f(p) \leq k$ ② [less]: $f(p) > k$	}	KB-AB _{LL} ⁴ , KB-LL ⁴ , KB-AB _{SB}
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- A-buffer simulation: $K^+B = \text{FreePipe}^5$ & $K^+B\text{-}SB = SB^6$

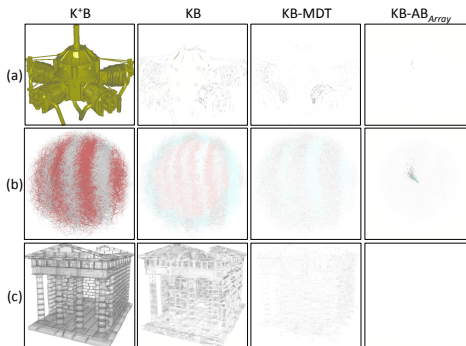
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Image Quality Analysis

Noticeable image differences from K^+B

Scenario (a) Z-buffer, (b) k-buffer, (c) A-buffer

Method KB¹: [RMW hazards], KB-MDT²: [depth conversion],
KB-AB_{Array}: [fragment overflow]



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- ③ explore dynamic k^+ -buffer: k value is not the same for all pixels

Thank you! - Questions?

Downloadable Source Code

GLSL shaders for all presented & tested methods are available at:
<http://cgrg.cs.uoi.gr/k+-buffer.php>

Acknowledgements

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European Union
European Social Fund

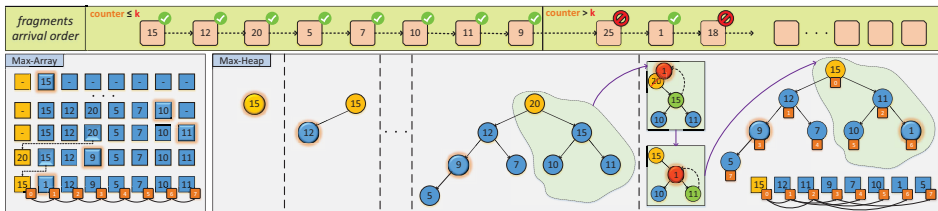


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k-buffer store example

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k-buffer methods details

[go back](#)

Algorithm		Performance	Sorting need		Peeling Accuracy		Memory	
Acronym	Description	Rendering Passes	on primitives	on fragments	Max k	32bit Float Precision	Per Pixel Allocation	Fixed
KB	Initial k -buffer implementation [Callahan2005,Bavoil2007]	1	✓	✓	8; 16	✓	2k; 4k	✓
KB-Multi	Multi-pass k -buffer [Liu2009a]	1 to k	✓	✓			2k; 4k	
KB-SR	Stencil routed k -buffer [Bavoil2008]	1	✓	✓	32		3k	
KB-PS	k -buffer using pixel synchronization extension [Salvi2013]	1	x	✓	-		2k	
K ⁺ B-Array	k^+ -buffer using max-array	1	x	✓	-		2k + 2	
K ⁺ B-Heap	k^+ -buffer using max-heap	1	x	✓	-		2k + 2	
KB-MDT	Multi depth test scheme [Liu2010,Maule2013]	2	x	x	-	x	2k	x
KB-MHA	Memory-hazard-aware k -buffer [Zhang2013]	1	✓	✓	8; 16		2k; 4k	
KB-AB _{Array}	k -buffer based on A-buffer (fixed-size arrays)	1	x	✓	-	✓	2n + 1	x
KB-AB _{LL}	k -buffer based on A-buffer (dynamic linked lists) [Yu2012]	1	x	✓	-		3f + 1	
KB-LL	k -buffer based on linked lists [Yu2012]	1	x	x	-		3f + 6	
KB-AB _{SB}	k -buffer based on S-buffer (variable-contiguous regions)	2	x	✓	-		2f + 2	
K ⁺ B-SB	Memory-friendly variation of k^+ -buffer	2	x	✓	-		2f _k + 3	
f(p) = # fragments at pixel p[x,y]		n = max _{x,y} {f(p)}			In A ; B, A denotes the layers/memory for the basic method and B for the variation using attribute packing			
f _k (p) = (f(p) < k) ? f(p) : k		f _k (p) ≤ k						